#### **FEATURES**

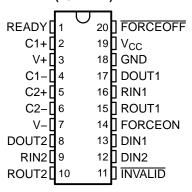
- ESD Protection for RS-232 I/O Pins
  - ±15 kV (Human-Body Model)
  - ±8 kV (IEC 61000, Contact Discharge)
  - ±8 kV (IEC 61000, Air-Gap Discharge)
- 300-μA Operating Supply Current
- 1-μA Low-Power Standby (With Receivers Active) Mode
- Designed to Transmit at a Data Rate of 460 kbps
- Auto-Power-Down Plus Option Features Flexible Power-Saving Mode
- Operates From a Single 2.25-V to 3-V V<sub>CC</sub> Supply

#### **APPLICATIONS**

- Battery-Powered Systems
- PDAs

- Cellular Phones
- Notebooks
- Hand-Held Equipment
- Pagers

#### DB OR PW PACKAGE (TOP VIEW)



#### **DESCRIPTION/ORDERING INFORMATION**

The MAX3318 is a dual-driver, dual-receiver, RS-232 compatible transceiver. The device features auto-power-down plus and enhanced electrostatic discharge (ESD) protection integrated into the chip. Driver output and receiver input are protected to  $\pm 8$  kV using the IEC 61000 Air-Gap Discharge method,  $\pm 8$  kV using the IEC 61000 Contact Discharge method, and  $\pm 15$  kV using the Human-Body Model (HBM).

The device operates at a data rate of 460 kbps. The transceiver has a proprietary low-dropout driver output stage enabling RS-232-compatible operation from a 2.25-V to 3-V supply with a dual charge pump. The charge pump requires only four  $0.1-\mu F$  capacitors and features a logic-level output (READY) that asserts when the charge pump is regulating and the device is ready to begin transmitting.

The MAX3318 achieves a  $1-\mu A$  supply current using the auto-power-down feature. This device automatically enters a low-power power-down mode when the RS-232 cable is disconnected or the drivers of the connected peripherals are inactive for more than 30 s. The device turns on again when it senses a valid transition at any driver or receiver input. Auto power down saves power without changes to the existing BIOS or operating system.

This device is available in two space-saving packages: 20-pin SSOP and 20-pin TSSOP.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

# MAX3318 2.5-V 460-kbps RS-232 TRANSCEIVER WITH ±15-kV ESD PROTECTION

SLLS687-OCTOBER 2005



#### ORDERING INFORMATION

T <sub>A</sub>	PACKA	GE <sup>(1)</sup>	ORDERABLE PART NUMBER	TOP-SIDE MARKING
	SSOP – DB	Tube of 70	MAX3318CDB	MA3318C
–0°C to 70°C	330F - DB	Reel of 2000	MAX3318CDBR	WASSING
-0 C to 70 C	TSSOP – PW	Tube of 70	MAX3318CPW	MA3318C
	1330P – PW	Reel of 2000	MAX3318CPWR	WA3316C
	0000 00	Tube of 70	MAX3318IDB	MA3318I
-40°C to 85°C	SSOP – DB	Reel of 2000	MAX3318IDBR	WASSIOI
-40 C to 65 C	TSSOP – PW	Tube of 70	MAX3318IPW	MA3318I
	1330F - FW	Reel of 2000	MAX3318IPWR	IVIAGGIOI

<sup>(1)</sup> Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

#### **DETAILED DESCRIPTION**

Flexible control options for power management are featured when the serial port and driver inputs are inactive. The auto-power-down plus feature functions when FORCEON is low and  $\overline{\text{FORCEOFF}}$  is high. During this mode of operation, if the device does not sense valid signal transitions on all receiver and driver inputs for approximately 30 s, the built-in charge pump and drivers are powered down, reducing the supply current to 1  $\mu$ A. By disconnecting the serial port or placing the peripheral drivers off, auto-power-down plus can be disabled when FORCEON and  $\overline{\text{FORCEOFF}}$  are high. With auto-power-down plus enabled, the device activates automatically when a valid signal is applied to any receiver or driver input.  $\overline{\text{INVALID}}$  is high (valid data) if any receiver input voltage is greater than 2.7 V or less than -2.7 V, or has been between -0.3 V and 0.3 V for less than 30  $\mu$ s (typical number).  $\overline{\text{INVALID}}$  is low (invalid data) if all receiver input voltage are between -0.3 V and 0.3 V for more than 30  $\mu$ s (typical number).



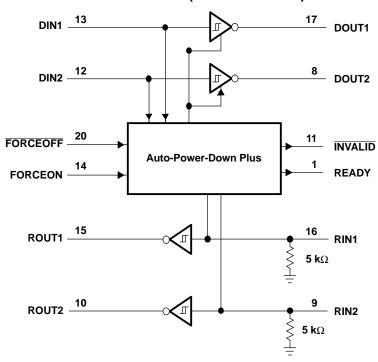
# FUNCTION TABLE<sup>(1)</sup>

	INPUT CONDITIONS				OUTPUT	STATES		
FORCEON	FORCEOFF	RECEIVER OR DRIVER EDGE WITHIN 30 s	VALID RS-232 LEVEL PRESENT AT RECEIVER	DRIVER	RECEIVER	INVALID	READY	OPERATING MODE
			Auto-Power-D	own Plus Co	onditions			
Н	Н	No	No	Active	Active	L	Н	Normal operation, auto-power-down plus disabled
Н	Н	No	Yes	Active	Active	Н	Н	Normal operation, auto-power-down plus disabled
L	Н	Yes	No	Active	Active	L	Н	Normal operation, auto-power-down plus enabled
L	Н	Yes	Yes	Active	Active	Н	Н	Normal operation, auto-power-down plus enabled
L	Н	No	No	Z	Active	L	L	Power down, auto-power-down plus enabled
L	Н	No	Yes	Z	Active	Н	L	Power down, auto-power-down plus enabled
X	L	Х	No	Z	Active	L	L	Manual power down
X	L	Х	Yes	Z	Active	Н	L	Manual power down
	Auto-Power-Down Conditions							
INVALID	ĪNVALID	Х	No	Z	Active	L	L	Power down, auto power down enabled
INVALID	ĪNVALID	х	Yes	Active	Active	Н	Н	Normal operation, auto power down enabled

<sup>(1)</sup> H = high level, L = low level, X = irrelevant, Z = high impedance



# **LOGIC DIAGRAM (POSITIVE LOGIC)**





## **TERMINAL FUNCTIONS**

TERMIN	NAL	DESCRIPTION
NAME	NO.	DESCRIPTION
C1+	2	Positive voltage-doubler charge-pump capacitor
C1-	4	Negative voltage-doubler charge-pump capacitor
C2+	5	Positive inverting charge-pump capacitor
C2-	6	Negative inverting charge-pump capacitor
DIN	12, 13	CMOS driver inputs
DOUT	8, 17	RS-232 driver outputs
FORCEOFF	20	Force-off input, active low. Drive low to power down transmitters, receivers, and charge pump. This overrides auto power down and FORCEON (see Function Table).
FORCEON	14	Force-on input, active high. Drive high to override auto power down, keeping transmitters and receivers on (FORCEOFF must be high) (see Function Table).
GND	18	Ground
INVALID	11	Valid signal detector output, active low. A logic high indicates that a valid RS-232 level is present on a receiver input.
READY	1	Ready to transmit output, active high. READY is enabled high when V– goes below –3.5 V and the device is ready to transmit.
RIN	9, 16	RS-232 receiver inputs
ROUT	10, 15	CMOS receiver outputs
V+	3	$2 \times V_{CC}$ generated by the charge pump
V-	7	$-2 \times V_{CC}$ generated by the charge pump
V <sub>CC</sub>	19	2.25-V to 3-V single-supply voltage

# MAX3318 2.5-V 460-kbps RS-232 TRANSCEIVER WITH $\pm$ 15-kV ESD PROTECTION

SLLS687-OCTOBER 2005



# Absolute Maximum Ratings(1)

over operating free-air temperature range (unless otherwise noted)

		MIN	MAX	UNIT
V <sub>CC</sub> to GND		-0.3	6	V
V+ to GND <sup>(2)</sup>		-0.3	7	V
V- to GND <sup>(2)</sup>		-7	0.3	V
V+ + IV-I <sup>(2)</sup>			13	V
Input voltogo	DIN, FORCEON, FORCEOFF to GND	-0.3	6	V
Input voltage	RIN to GND	-0.3	"	
Outrot valta as	DOUT to GND		V	
Output voltage	ROUT, INVALID, READY to GND	-0.3	V <sub>CC</sub> + 0.3	V
Short-circuit duration	DOUT to GND		Continuous	
	16-pin SSOP (derate 7.14 mW/°C above 70°C)		571	
Continuous power dissipation (T <sub>A</sub> = 70°C)	20-pin SSOP (derate 8 mW/°C above 70°C		640	mW
	20-pin TSSOP (derate 7 mW/°C above 70°C)	13 -0.3 6 ±25 ±13.2 -0.3 V <sub>CC</sub> + 0.3 Continuous 571		
Storage temperature range		-65	150	°C
Lead temperature (soldering, 10 s)			300	°C

<sup>(1)</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

#### **Recommended Operating Conditions**

See Figure 4

				MIN	NOM	MAX	UNIT
	Supply voltage			2.25	2.5	3	V
V <sub>IH</sub>	Driver and control high-level input voltage	DIN, FORCEOFF, FORCEON	V <sub>CC</sub> = 2.5 V to 3 V	$0.7 \times V_{CC}$		5.5	٧
V <sub>IL</sub>	Driver and control low-level input voltage	DIN, FORCEOFF, FORCEON	V <sub>CC</sub> = 2.5 V to 3 V	0		$0.3 \times V_{CC}$	٧
$V_{I}$	Receiver input voltage			-25		25	٧
т	Operating free-air temperature	MAX3318C		0		70	ĵ
IA		MAX3318I		-40		85	

<sup>(2)</sup> V+ and V- can have maximum magnitudes of 7 V, but their absolute difference cannot exceed 13 V.



# **Supply Current Section Electrical Characteristics**

 $V_{CC}$  = 2.25 V to 3 V, C1–C4 = 0.1  $\mu\text{F}$  ,  $T_{\text{A}}$  =  $T_{\text{MIN}}$  to  $T_{\text{MAX}}$  (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP <sup>(1)</sup>	MAX	UNIT
DC Characteristics ( $V_{CC} = 2.5 \text{ V}, T_A$	= 25°C)				
Auto-power-down plus supply current	FORCEON = GND, FORCEOFF = V <sub>CC</sub> , All RIN and DIN idle		1	10	μΑ
Auto-power-down supply current	FORCEOFF = GND		1	10	μΑ
Supply current	FORCEON = FORCEOFF = V <sub>CC</sub> , No load		0.3	2	mA

<sup>(1)</sup> Typical values are at  $V_{CC}$  = 2.5 V,  $T_A$  = 25°C.

## **ESD Protection**

PARAMETER	TEST CONDITIONS	TYP	UNIT
	Human-Body Model (HBM)	±15	
RIN, DOUT	IEC G1000-4-2 Air-Gap Discharge method	±8	kV
	IEC G1000-4-2 Contact Discharge method	±8	

# **MAX3318** 2.5-V 460-kbps RS-232 TRANSCEIVER WITH ±15-kV ESD PROTECTION





#### **Driver Section Electrical Characteristics**

over recommended ranges of supply voltage and operating free-air temperature,

 $V_{CC}$  = 2.25 V to 3 V, C1–C4 = 0.1  $\mu$ F,  $T_A$  =  $T_{MIN}$  to  $T_{MAX}$  (unless otherwise noted) (see Figure 4)

PARAMETER	TEST CONDITIONS	MIN	TYP <sup>(1)</sup>	MAX	UNIT
Driver input hysteresis			0.3		V
Input leakage current	FORCEON, DIN, FORCEOFF		±0.01	±1	μΑ
Output voltage swing	All driver outputs loaded with 3 kΩ to ground	±3.7	±4		V
Output resistance	$V_{CC} = 0$ , Driver output = $\pm 2 \text{ V}$	300	10M		Ω
Output short-circuit current <sup>(2)</sup>			±25	±60	mA
Output leakage current	$V_{CC}$ = 0 or 2.25 V to 3 V, $V_{OUT}$ = ±12 V, Drivers disabled			±25	μΑ

Typical values are at  $V_{CC} = 2.5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ .

#### **Driver Section Switching Characteristics**

over recommended ranges of supply voltage and operating free-air temperature,

 $V_{CC}$  = 2.25 V to 3 V, C1–C4 = 0.1  $\mu$ F,  $T_A$  =  $T_{MIN}$  to  $T_{MAX}$  (unless otherwise noted) (see Figure 1)

	PARAMETER	TEST CONDITIONS	MIN	TYP <sup>(1)</sup>	MAX	UNIT
	Maximum data rate	$R_L = 3 \text{ k}\Omega$ , $C_L = 1000 \text{ pF}$ , One transmitter switching	460			kbps
t <sub>PHL</sub> - t <sub>PLH</sub>	Driver skew <sup>(2)</sup>			100		ns
	Transition-region slew rate	$V_{CC} = 2.5 \text{ V}, T_A = 25^{\circ}\text{C}, R_L = 3 \text{ k}\Omega \text{ to } 7 \text{ k}\Omega,$ Measured from 3 V to -3 V or -3 V to 3 V, $C_L = 150 \text{ pF}$ to 2500 pF	4		30	V/μs

Short-circuit durations should be controlled to prevent exceeding the device absolute power dissipation ratings, and not more than one output should be shorted at a time.

<sup>(1)</sup> Typical values are at  $V_{CC}$  = 2.5 V,  $T_A$  = 25°C. (2) Pulse skew is defined as  $|t_{PLH} - t_{PHL}|$  of each channel of the same device.



#### **Receiver Section Electrical Characteristics**

over recommended ranges of supply voltage and operating free-air temperature,

 $V_{CC}$  = 2.25 V to 3 V, C1–C4 = 0.1  $\mu$ F,  $T_A$  =  $T_{MIN}$  to  $T_{MAX}$  (unless otherwise noted) (see Figure 4)

PARAMETER	TEST CONDITIONS	MIN	TYP <sup>(1)</sup>	MAX	UNIT
Input voltage range		-25		25	V
Input threshold low	T <sub>A</sub> = 25°C			$0.3 \times V_{CC}$	V
Input threshold high	T <sub>A</sub> = 25°C	$0.7 \times V_{CC}$			V
Input hysteresis			0.3		V
Input resistance	T <sub>A</sub> = 25°C	3	5	7	kΩ
Output leakage current			±0.05	±10	μΑ
Output voltage low	I <sub>OUT</sub> = 0.5 mA			$0.1 \times V_{CC}$	V
Output voltage high	$I_{OUT} = -0.5 \text{ mA}$	0.9 × V <sub>CC</sub>			V

<sup>(1)</sup> Typical values are at  $V_{CC}$  = 2.5 V,  $T_A$  = 25°C.

#### **Receiver Section Switching Characteristics**

over recommended ranges of supply voltage and operating free-air temperature,

 $V_{CC}$  = 2.25 V to 3 V, C1–C4 = 0.1  $\mu$ F,  $T_A$  =  $T_{MIN}$  to  $T_{MAX}$  (unless otherwise noted) (see Figure 4)

	PARAMETER	TEST CONDITIONS	TYP <sup>(1)</sup>	UNIT
t <sub>PHL</sub>	Descriver propagation delay	DIN to DOUT C 450 of	0.175	
t <sub>PLH</sub>	Receiver propagation delay	RIN to ROUT, C <sub>L</sub> = 150 pF	0.175	μs
t <sub>PHL</sub> - t <sub>PLH</sub>	Receiver skew <sup>(2)</sup>		50	ns

Typical values are at  $V_{CC}$  = 2.5 V,  $T_A$  = 25°C. Pulse skew is defined as  $|t_{PLH}-t_{PHL}|$  of each channel of the same device.



#### **Auto-Power-Down Plus Section Electrical Characteristics**

over recommended ranges of supply voltage and operating free-air temperature,

 $V_{CC}$  = 2.25 V to 3 V, C1–C4 = 0.1  $\mu$ F,  $T_A$  =  $T_{MIN}$  to  $T_{MAX}$  (unless otherwise noted) (see Figure 4)

PARAMETER	TEST CONDITIONS	MIN	MAX	UNIT
Receiver input threshold to INVALID high	Positive threshold		2.7	\/
Receiver input threshold to invalid high	Negative threshold	-2.7	V	
Receiver input threshold INVALID low		-0.3	0.3	V
ĪNVALID, READY voltage low	I <sub>OUT</sub> = 0.5 mA		$0.1 \times V_{CC}$	V
ĪNVALID, READY voltage high	$I_{OUT} = -0.5 \text{ mA}$	$0.8 \times V_{CC}$		V

## **Auto-Power-Down Plus Section Switching Characteristics**

over recommended ranges of supply voltage and operating free-air temperature,

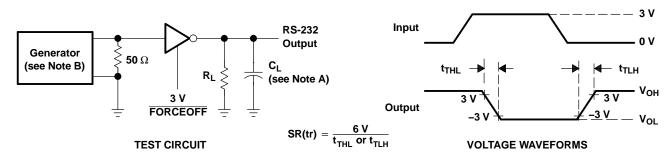
 $V_{CC}$  = 2.25 V to 3 V, C1–C4 = 0.1  $\mu$ F,  $T_A$  =  $T_{MIN}$  to  $T_{MAX}$  (unless otherwise noted) (see Figure 4)

	PARAMETER	TEST CONDITIONS	MIN	TYP <sup>(1)</sup>	MAX	UNIT
t <sub>INVH</sub>	Receiver positive or negative threshold to INVALID high	V <sub>CC</sub> = 2.5 V		1		μs
t <sub>INVL</sub>	Receiver positive or negative threshold to INVALID low	V <sub>CC</sub> = 2.5 V		30		μs
t <sub>WU</sub>	Receiver or driver edge to driver enabled	V <sub>CC</sub> = 2.5 V		100		μs
t <sub>AUTOPRDN</sub>	Receiver or driver edge to driver shutdown	V <sub>CC</sub> = 2.5 V	15	30	60	S

(1) Typical values are at  $V_{CC} = 2.5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ .



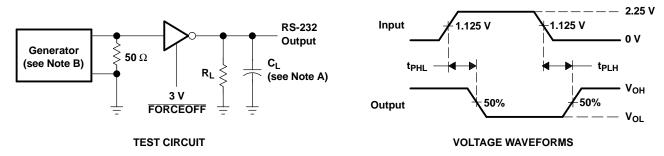
#### PARAMETER MEASUREMENT INFORMATION



NOTES: A. C<sub>L</sub> includes probe and jig capacitance.

B. The pulse generator has the following characteristics: PRR = 250 kbit/s,  $Z_O = 50~\Omega$ , 50% duty cycle,  $t_r \le 10$  ns,  $t_f \le 10$  ns.

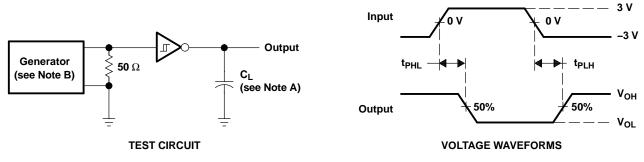
Figure 1. Driver Slew Rate



NOTES: A.  $C_L$  includes probe and jig capacitance.

B. The pulse generator has the following characteristics: PRR = 250 kbit/s,  $Z_{O}$  = 50  $\Omega$ , 50% duty cycle,  $t_{f}$   $\leq$  10 ns,  $t_{f}$   $\leq$  10 ns.

Figure 2. Driver Pulse Skew



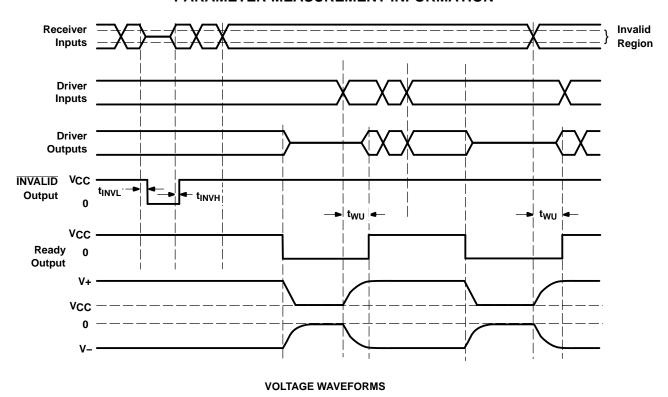
NOTES: A.  $C_L$  includes probe and jig capacitance.

B. The pulse generator has the following characteristics:  $Z_0 = 50 \ \Omega$ , 50% duty cycle,  $t_f \le 10 \ ns$ .

Figure 3. Receiver Propagation Delay Times



#### PARAMETER MEASUREMENT INFORMATION



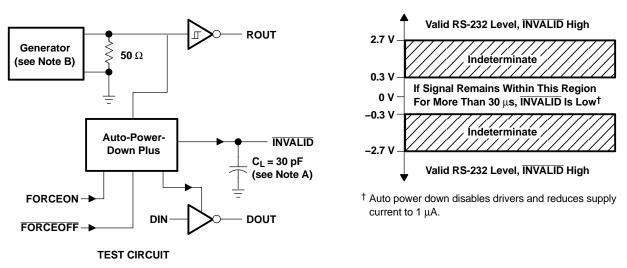
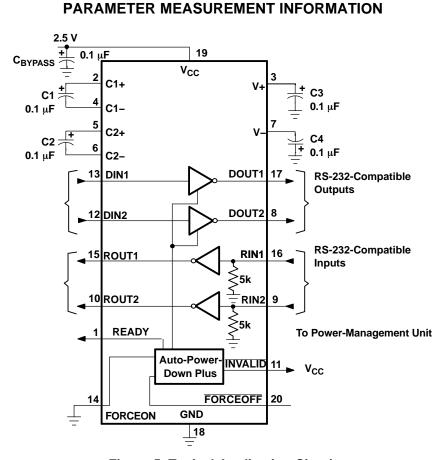


Figure 4. INVALID Propagation Delay Times and Supply Enabling Time







**Figure 5. Typical Application Circuit** 





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#### **PACKAGING INFORMATION**

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Top-Side Markings	Samples
MAX3318CDB	ACTIVE	SSOP	DB	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	MP318C	Samples
MAX3318CDBE4	ACTIVE	SSOP	DB	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	MP318C	Samples
MAX3318CDBG4	ACTIVE	SSOP	DB	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	MP318C	Samples
MAX3318CDBR	ACTIVE	SSOP	DB	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	MP318C	Samples
MAX3318CDBRE4	ACTIVE	SSOP	DB	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	MP318C	Samples
MAX3318CDBRG4	ACTIVE	SSOP	DB	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	MP318C	Samples
MAX3318CPW	ACTIVE	TSSOP	PW	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	MP318C	Samples
MAX3318CPWE4	ACTIVE	TSSOP	PW	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	MP318C	Samples
MAX3318CPWG4	ACTIVE	TSSOP	PW	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	MP318C	Samples
MAX3318CPWR	ACTIVE	TSSOP	PW	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	MP318C	Samples
MAX3318CPWRE4	ACTIVE	TSSOP	PW	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	MP318C	Samples
MAX3318CPWRG4	ACTIVE	TSSOP	PW	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	0 to 70	MP318C	Samples
MAX3318IDB	ACTIVE	SSOP	DB	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	MP318I	Samples
MAX3318IDBE4	ACTIVE	SSOP	DB	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	MP318I	Samples
MAX3318IDBG4	ACTIVE	SSOP	DB	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	MP318I	Samples
MAX3318IDBR	ACTIVE	SSOP	DB	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	MP318I	Samples
MAX3318IDBRE4	ACTIVE	SSOP	DB	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	MP318I	Samples





www.ti.com 24-Jan-2013

Orderable Device	Status	Package Type	_	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Top-Side Markings	Samples
	(1)		Drawing			(2)		(3)		(4)	
MAX3318IDBRG4	ACTIVE	SSOP	DB	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	MP318I	Samples
MAX3318IPW	ACTIVE	TSSOP	PW	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	MP318I	Samples
MAX3318IPWE4	ACTIVE	TSSOP	PW	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	MP318I	Samples
MAX3318IPWG4	ACTIVE	TSSOP	PW	20	70	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	MP318I	Samples
MAX3318IPWR	ACTIVE	TSSOP	PW	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	MP318I	Samples
MAX3318IPWRE4	ACTIVE	TSSOP	PW	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	MP318I	Samples
MAX3318IPWRG4	ACTIVE	TSSOP	PW	20	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	-40 to 85	MP318I	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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<sup>&</sup>lt;sup>(4)</sup> Only one of markings shown within the brackets will appear on the physical device.



# **PACKAGE OPTION ADDENDUM**

24-Jan-2013

continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

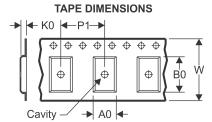
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# **PACKAGE MATERIALS INFORMATION**

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## TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



#### \*All dimensions are nominal

All dimensions are nominal												
Device	_	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
MAX3318CDBR	SSOP	DB	20	2000	330.0	16.4	8.2	7.5	2.5	12.0	16.0	Q1
MAX3318CPWR	TSSOP	PW	20	2000	330.0	16.4	6.95	7.1	1.6	8.0	16.0	Q1
MAX3318IDBR	SSOP	DB	20	2000	330.0	16.4	8.2	7.5	2.5	12.0	16.0	Q1
MAX3318IPWR	TSSOP	PW	20	2000	330.0	16.4	6.95	7.1	1.6	8.0	16.0	Q1

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\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
MAX3318CDBR	SSOP	DB	20	2000	367.0	367.0	38.0
MAX3318CPWR	TSSOP	PW	20	2000	367.0	367.0	38.0
MAX3318IDBR	SSOP	DB	20	2000	367.0	367.0	38.0
MAX3318IPWR	TSSOP	PW	20	2000	367.0	367.0	38.0

PW (R-PDSO-G20)

# PLASTIC SMALL OUTLINE



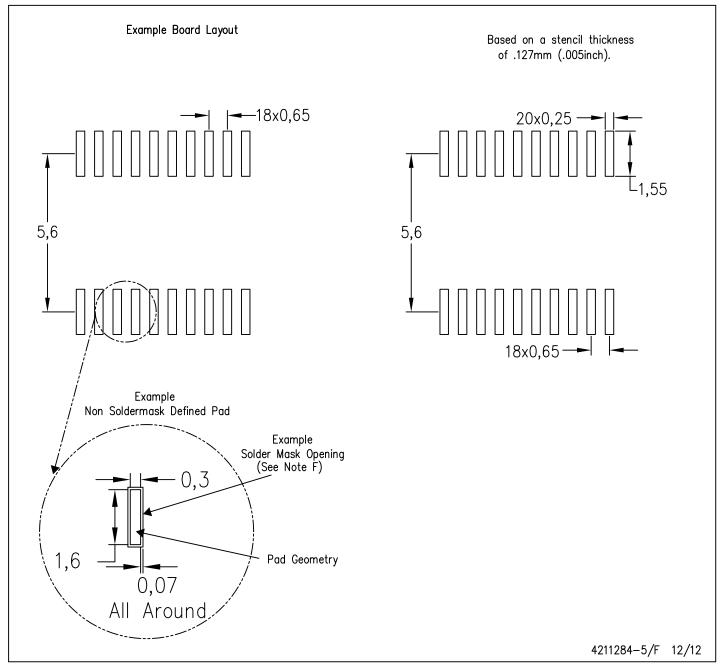
NOTES:

- A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M—1994.
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0,15 each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0,25 each side.
- E. Falls within JEDEC MO-153



# PW (R-PDSO-G20)

# PLASTIC SMALL OUTLINE



NOTES:

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Publication IPC-7351 is recommended for alternate design.
- D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
- E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.



## DB (R-PDSO-G\*\*)

## PLASTIC SMALL-OUTLINE

#### **28 PINS SHOWN**



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.

D. Falls within JEDEC MO-150

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