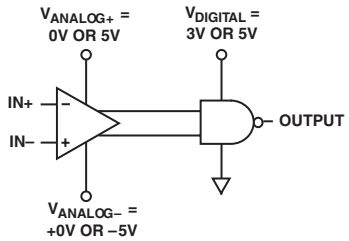
23 ns and 65 ns Low Voltage Comparators

CMP401/CMP402

FEATURES
23 ns or 65 ns Propagation Delay
Single-Supply Operation
Compatible with 3 V and 5 V Logic
Separate Input and Output Sections
Low Power
Wide Input Range: –5 V to +3.9 V

APPLICATIONS
Battery-Operated Instrumentation
Line Receivers
Level Translators
Read Channel Detection

FUNCTIONAL BLOCK DIAGRAM

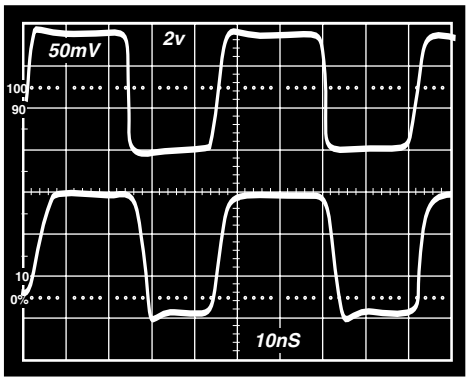


NOTE: $(V_{ANALOG+}) - (V_{ANALOG–}) \geq 3V$

GENERAL DESCRIPTION

The CMP401 and CMP402 are 23 ns and 65 ns quad comparators with separate input and output supplies. Separate supplies enable the input stage to be operated from +3 V to as high as ±6 V. The output can be supplied with either 3 V or 5 V as determined by the interface logic or available supplies. Independent input and output supplies combined with fast propagation make the CMP401 and CMP402 excellent choices for interfacing to portable instrumentation.

The CMP401 and CMP402 are specified over the extended industrial (–40°C to +125°C) temperature range. Both are available in narrow SO-16 surface-mount packages and 16-lead TSSOP.



CMP401: 20 MHz Noninverting Switching, V_{IN} = ±100 mV

REV. A

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CMP401/CMP402–SPECIFICATIONS

ELECTRICAL SPECIFICATIONS (@ $V+_{ANA} = V+_{DIG} = 5.0$ V, $V_{CM} = 0.1$ V, $-40°C \le T_A \le +125°C$, unless otherwise noted.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
INPUT CHARACTERISTICS						
Offset Voltage[1]	V_{OS}	$T_A = 25°C$			3	mV
	V_{OS}				4	mV
Hysteresis				2		mV
Input Bias Current	I_B	$T_A = 25°C$			3	µA
	I_B				4	µA
Input Offset Current	I_{OS}				±3	µA
Input Common-Mode Voltage Range	V_{CM}		0		4.0	V
Common-Mode Rejection	CMRR	$0.1 V \le V_{CM} \le 3.9 V$	60			dB
Large-Signal Voltage Gain	A_{VO}	$R_L = 10 k\Omega$		10		V/mV
Offset Voltage Drift	$\Delta V_{OS}/\Delta T$			1		µV/°C
OUTPUT CHARACTERISTICS						
Output High Voltage	V_{OH}	$I_{OH} = -3.2$ mA	4.6			V
Output Low Voltage	V_{OL}	$I_{OL} = 3.2$ mA			0.2	V
POWER SUPPLY						
Power Supply Rejection Ratio	PSRR	$V+_{ANA}$ and $V+_{DIG}$ 2.7 V to 6 V	60			dB
Analog Supply Current – CMP401	I_{ANA}	$T_A = 25°C$			6.5	mA
Digital Supply Current – CMP401	I_{DIG}	$V_O = 0$ V, $R_L = \infty$, $T_A = 25°C$			2.0	mA
Analog Supply Current – CMP401	I_{ANA}				8.0	mA
Digital Supply Current – CMP401	I_{DIG}	$V_O = 0$ V, $R_L = \infty$			2.25	mA
Analog Supply Current – CMP402	I_{ANA}	$T_A = 25°C$			1.4	mA
Digital Supply Current – CMP402	I_{DIG}	$V_O = 0$ V, $R_L = \infty$, $T_A = 25°C$			2.0	mA
Analog Supply Current – CMP402	I_{ANA}				1.75	mA
Digital Supply Current – CMP402	I_{DIG}	$V_O = 0$ V, $R_L = \infty$			2.25	mA
DYNAMIC PERFORMANCE						
Propagation Delay – CMP401	t_P	100 mV Step with 20 mV OD				
		$T_A = 25°C$		17	23	ns
	t_P	100 mV Step with 5 mV OD				
		$T_A = 25°C$		33		ns
	t_P	100 mV Step with 20 mV OD			30	ns
Propagation Delay – CMP402	t_P	100 mV Step with 20 mV OD				
		$T_A = 25°C$		54	65	ns
	t_P	100 mV Step with 5 mV OD				
		$T_A = 25°C$		60		ns
	t_P	100 mV Step with 20 mV OD			75	ns

ELECTRICAL SPECIFICATIONS (@ $V_{ANA} = V_{DIG} = 3.0$ V, $V_{CM} = 0.1$ V, $T_A = 25°C$, unless otherwise noted.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
INPUT CHARACTERISTICS						
Offset Voltage[1]	V_{OS}				4.5	mV
Input Common-Mode Voltage Range	V_{CM}		0		2.0	V
Input Differential Voltage Range	V_{DIFF}		±2.0			V
Common-Mode Rejection	CMRR	$0.1 V \le V_{CM} \le 1.9 V$	60			dB
OUTPUT CHARACTERISTICS						
Output High Voltage	V_{OH}	$I_{OH} = -3.2$ mA	2.6			V
Output Low Voltage	V_{OL}	$I_{OL} = 3.2$ mA			0.25	V
POWER SUPPLY						
Power Supply Rejection Ratio	PSRR	$V+_{ANA}$ and $V+_{DIG}$ 2.7 V to 6 V	60			dB
Analog Supply Current – CMP401	I_{ANA}				6	mA
Digital Supply Current – CMP401	I_{DIG}	$V_O = 0$ V, $R_L = \infty$			1	mA
Analog Supply Current – CMP402	I_{ANA}				1.2	mA
Digital Supply Current – CMP402	I_{DIG}	$V_O = 0$ V, $R_L = \infty$			1	mA
DYNAMIC PERFORMANCE						
Propagation Delay – CMP401	t_P	100 mV Step with 20 mV OD		32		ns
Propagation Delay – CMP402	t_P	100 mV Step with 20 mV OD		70		ns

ELECTRICAL SPECIFICATIONS (@ $V\pm_{ANA}$ = ±5 V, V_{DIG} = 5.0 V, T_A = 25°C, unless otherwise noted.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
INPUT CHARACTERISTICS						
Offset Voltage[1]	V_{OS}	V_{CM} = 0 V			3	mV
Input Common-Mode Voltage Range	V_{CM}		–5.0		+4.0	V
Input Differential Voltage Range	V_{DIFF}		±8.0			V
Common-Mode Rejection	CMRR	–4.9 V ≤ V_{CM} ≤ 3.9 V	60			dB
Offset Voltage Drift	$\Delta V_{OS}/\Delta T$			1	5	µV/°C
POWER SUPPLY						
Power Supply Rejection Ratio	PSRR	$V\pm_{ANA}$ ±3 V to ±6 V	60			dB
Analog Supply Current – CMP401	I_{ANA}				6.5	mA
Digital Supply Current – CMP401	I_{DIG}	V_O = 0 V, R_L = ∞			2.0	mA
Analog Supply Current – CMP402	I_{ANA}				2.0	mA
Digital Supply Current – CMP402	I_{DIG}	V_O = 0 V, R_L = ∞			2.0	mA
DYNAMIC PERFORMANCE						
Propagation Delay – CMP401	t_P	100 mV Step with 20 mV OD			23	ns
Propagation Delay – CMP402	t_P	100 mV Step with 20 mV OD			65	ns

NOTES

[1]Offset voltage is defined as $(V_{OS+} + V_{OS-})/2$.

Specifications subject to change without notice.

ABSOLUTE MAXIMUM RATINGS[1]

Total Analog Supply Voltage . . . . . 16 V
Digital Supply Voltage . . . . . 7 V
Analog Positive Supply—Digital Positive Supply . . . . –200 mV
Input Voltage[2] . . . . . ±7 V
Differential Input Voltage . . . . . ±9 V
Output Short-Circuit Duration to GND . . . . . Indefinite
Storage Temperature Range
S, RU Package . . . . . –65°C to +150°C
Operating Temperature Range
CMP401G, CMP402G . . . . . –40°C to +125°C
Junction Temperature Range
S, RU Package . . . . . –65°C to +150°C
Lead Temperature Range (Soldering 60 sec) . . . . . 300°C

Package Type	θ_{JA}[3]	θ_{JC}	Units
16-Lead SO (S)	113	37	°C/W
16-Lead TSSOP (RU)	180	37	°C/W

NOTES
[1]Absolute Maximum Ratings apply to packaged parts, unless otherwise noted.
[2]The analog input voltage is equal to ±7 V or the analog supply voltage, whichever is less.
[3]θ_{JA} is specified for the worst-case conditions, i.e., θ_{JA} is specified for device soldered in circuit board for SOIC and TSSOP packages.

ORDERING GUIDE

Model	Temperature Range	Package Description	Package Option
CMP401GS	–40°C to +125°C	16-Lead SOIC	R-16A
CMP401GRU	–40°C to +125°C	16-Lead TSSOP	RU-16
CMP402GS	–40°C to +125°C	16-Lead SOIC	R-16A
CMP402GRU	–40°C to +125°C	16-Lead TSSOP	RU-16

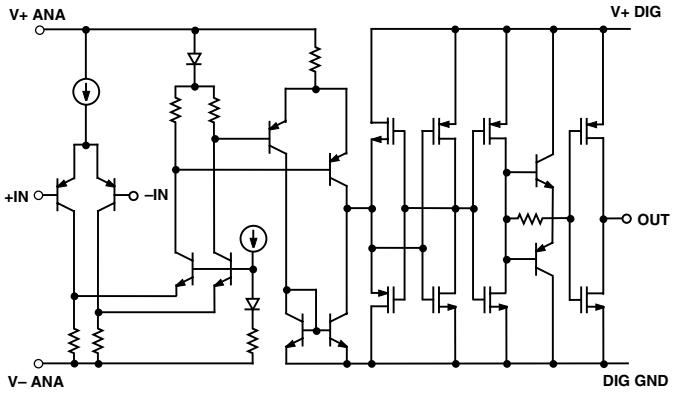
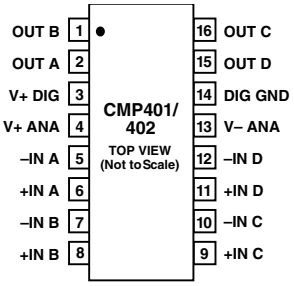


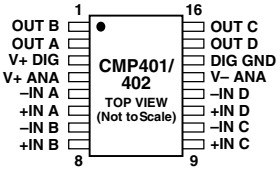
Figure 1. Simplified Schematic

PIN CONFIGURATIONS

16-Lead Narrow-SO (S-Suffix)



16-Lead TSSOP (RU-Suffix)

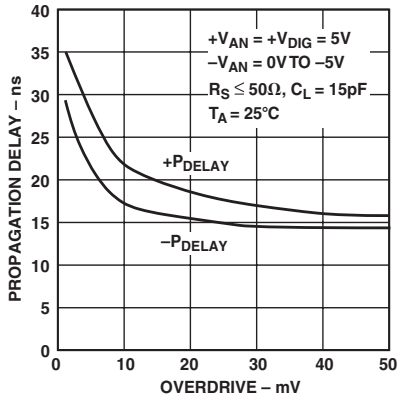


CAUTION

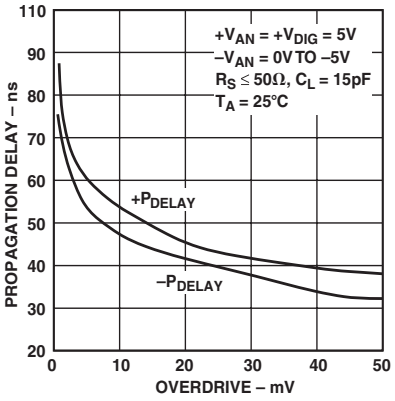
ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although the CMP401/CMP402 features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high-energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.



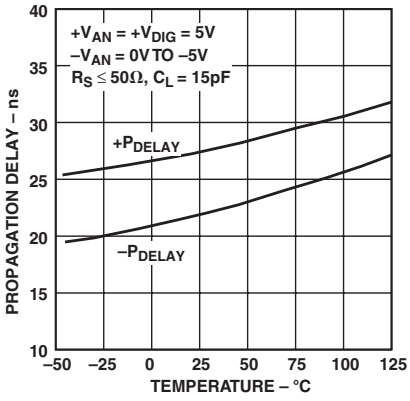
Typical Performance Characteristics–CMP401/CMP402



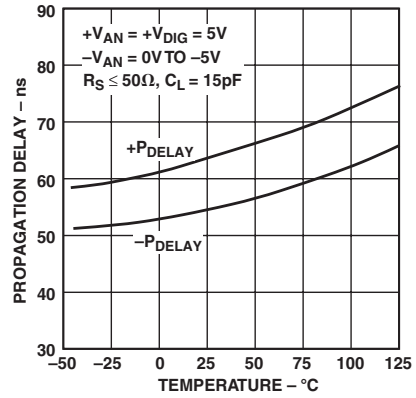
TPC 1. CMP401 Propagation Delay vs. Overdrive



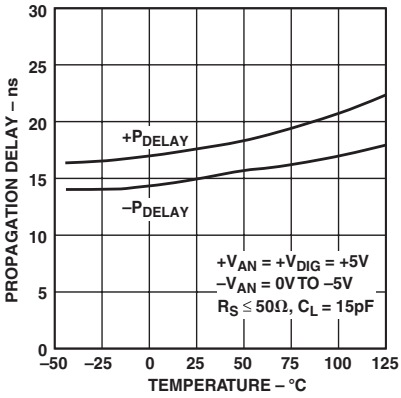
TPC 2. CMP402 Propagation Delay vs. Overdrive



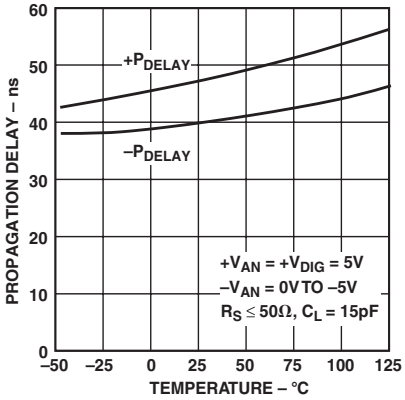
TPC 3. CMP401 Propagation Delay vs. Temperature – 5 mV OD



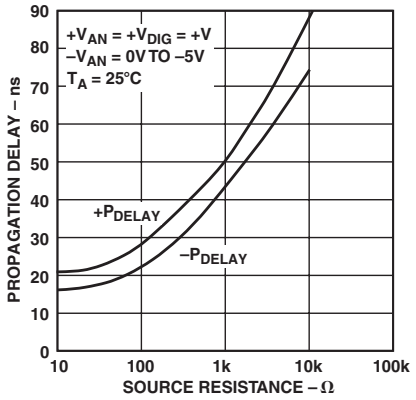
TPC 4. CMP402 Propagation Delay vs. Temperature – 5 mV OD



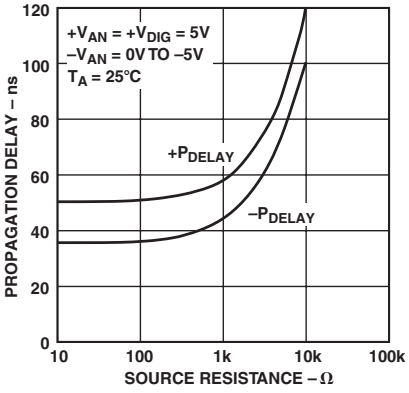
TPC 5. CMP401 Propagation Delay vs. Temperature – 20 mV OD



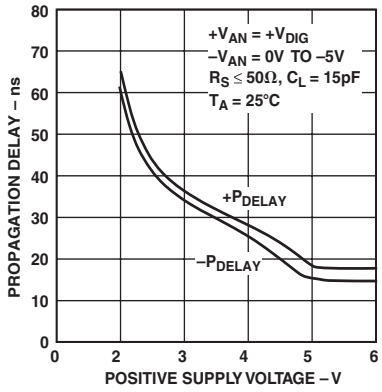
TPC 6. CMP402 Propagation Delay vs. Temperature – 20 mV OD



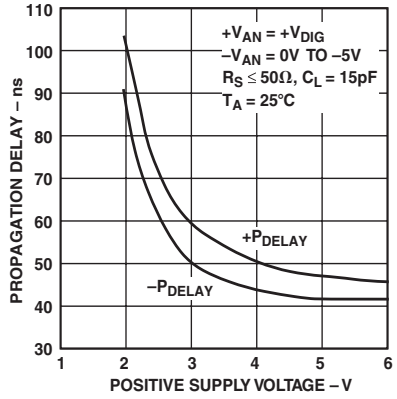
TPC 7. CMP401 Propagation Delay vs. Source Resistance – 20 mV OD



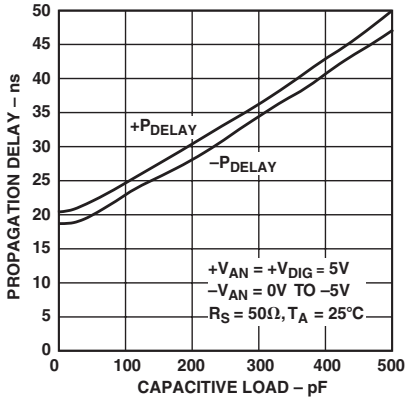
TPC 8. CMP402 Propagation Delay vs. Source Resistance – 20 mV OD



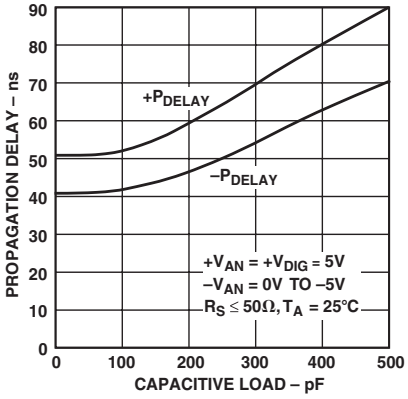
TPC 9. CMP401 Propagation Delay vs. Supply Voltage – 20 mV OD



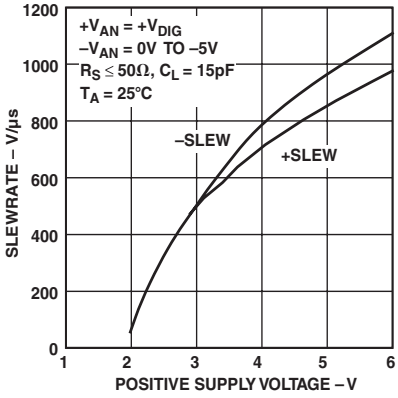
TPC 10. CMP402 Propagation Delay vs. Supply Voltage – 20 mV OD



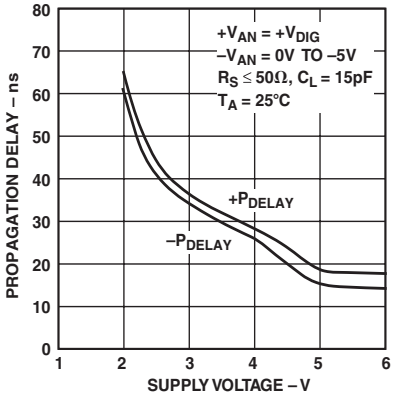
TPC 11. CMP401 Propagation Delay vs. Capacitive Load



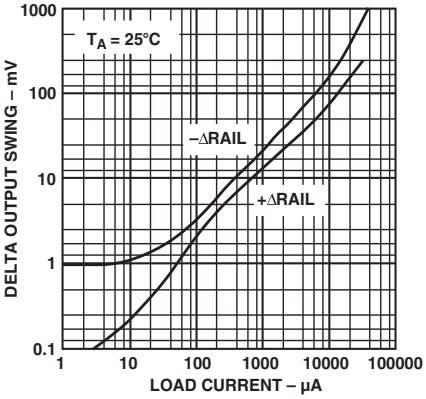
TPC 12. CMP402 Propagation Delay vs. Capacitive Load



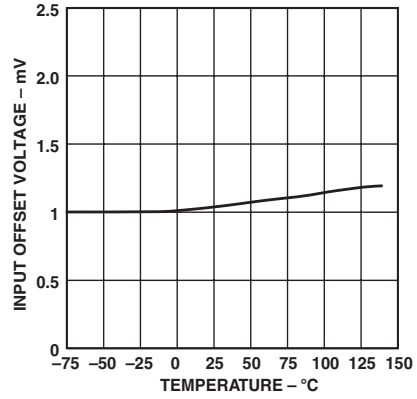
TPC 13. CMP401/CMP402 Slew Rate vs. Positive Supply Voltage



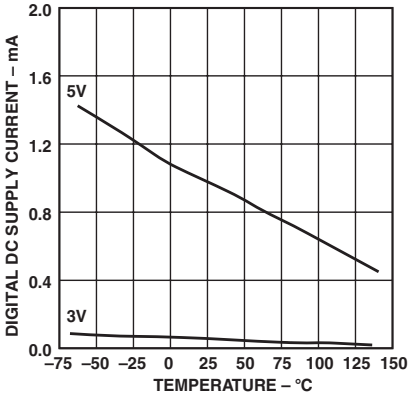
TPC 14. CMP401 Propagation Delay vs. Supply Voltage



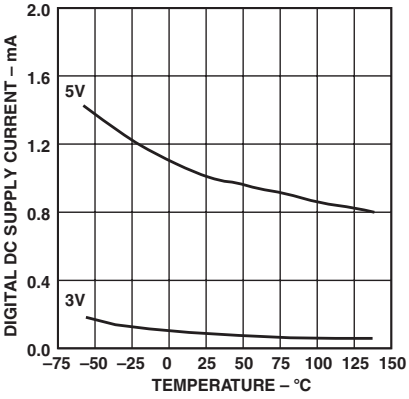
TPC 15. CMP401/CMP402 Delta Output Swing from Power Supplies vs. Load Current



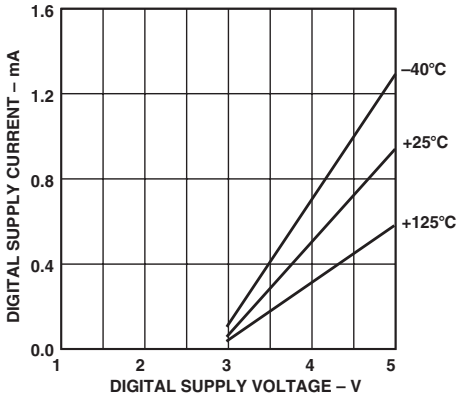
TPC 16. CMP401/CMP402 Input Offset Voltage vs. Temperature



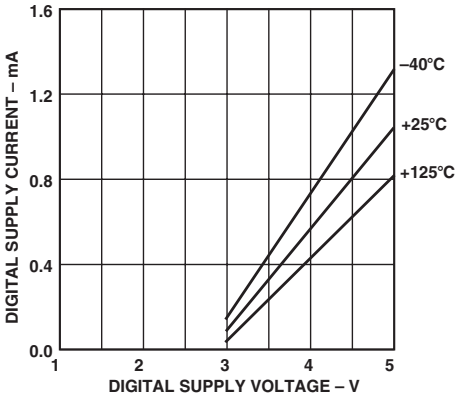
TPC 17. CMP401 Digital Supply Current vs. Temperature



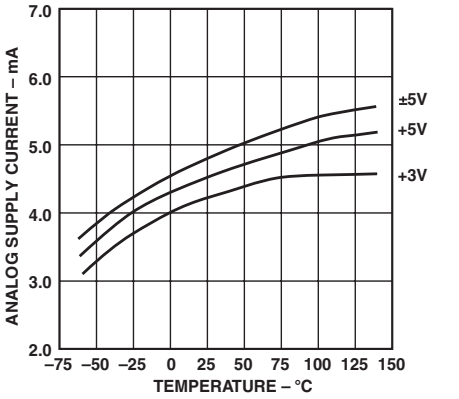
TPC 18. CMP402 Digital Supply Current vs. Temperature



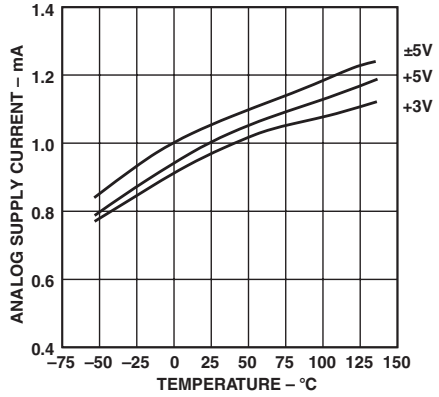
TPC 19. CMP401 Digital Supply Current vs. Digital Supply Voltage



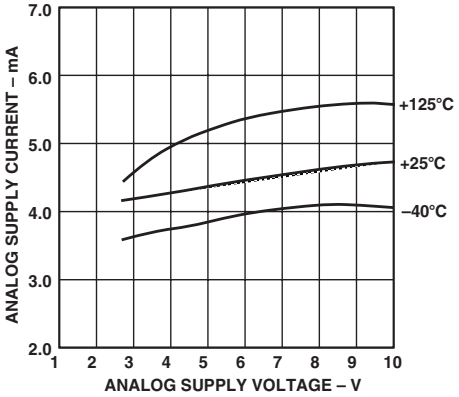
TPC 20. CMP402 Digital Supply Current vs. Digital Supply Voltage



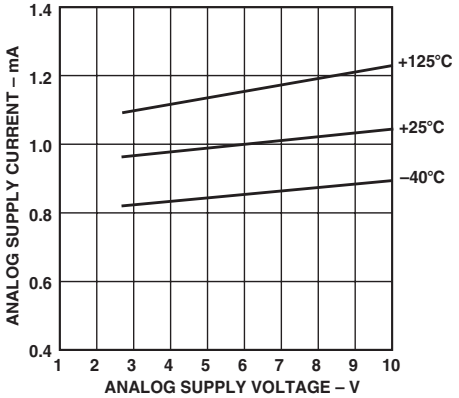
TPC 21. CMP401 Analog Supply Current vs. Temperature



TPC 22. CMP402 Analog Supply Current vs. Temperature



TPC 23. CMP401 Analog Supply Current vs. Analog Supply Voltage

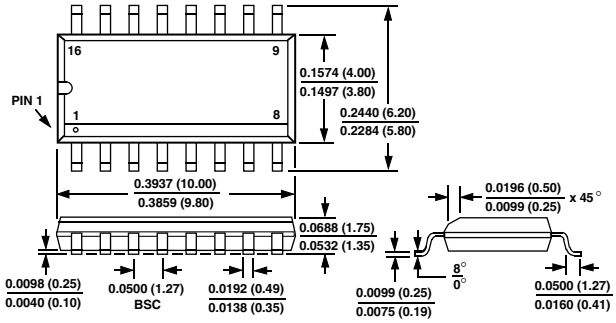


TPC 24. CMP402 Analog Supply Current vs. Analog Supply Voltage

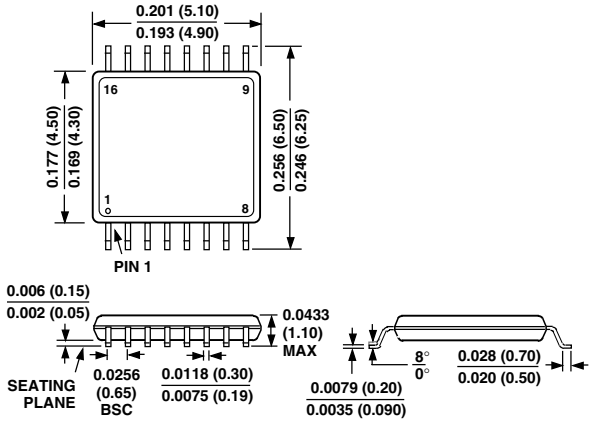
OUTLINE DIMENSIONS

Dimensions shown in inches and (mm).

16-Lead Narrow-SOIC
(R-16A)



16-Lead TSSOP
(RU-16)



Revision History

Location	Page
Data Sheet changed from REV. 0 to REV. A.	
Edits to GENERAL DESCRIPTION	1
Edits to ABSOLUTE MAXIMUM RATINGS	4
Edits to PACKAGE TYPE	4
Edits to ORDERING GUIDE	4
Deleted DICE CHARACTERISTICS	4
Edits to CMP401/CMP402 PIN CONFIGURATIONS	4

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